


FACSIMILE OF U.S. DEPARTMENT OF COMMERCE FORM PTO-1449 Patent and Trademark Office (REV. 6-89)				ATTORNEY'S DOCKET NUMBER UIL-1001-1		SERIAL NUMBER 09/160,657	
 INFORMATION DISCLOSURE CITATION (Use Several Sheets if Necessary)				APPLICANT Joseph W. Lyding et al.			
				FILING DATE September 25, 1998		GROUP ART UNIT 2822	

U.S. PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS						
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)		
MG		"Process Stability of Deuterium-Annealed MOSFET's," W.F. Clark, et al., IEEE Electron Device Letters, VOL. 20. No. 1, January 1999, pages 48-50.
MG		"Defect Generation in Field-Effect Transistors Under Channel-Hot-Electron Stress," D.J. DiMaria, Journal of Applied Physics, Vol. 87, No. 12, June 15, 2000, pages 8707-8715.

EXAMINER <i>Maria G. Herrera</i>	DATE CONSIDERED <i>1-25-01</i>
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